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Inventor Name Search Result

Your Search was:

Last Name = THAKKAR
First Name = SHREEKANT

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S
10663169	Not Issued	019	09/15/2003	EXTENDED STAND COMPUTER SYSTEM WITH RETRACTABLE KEYBOARD	THAKKAR, SHREEKANT SURYAKANT
10663013	Not Issued	019	09/15/2003	COMPUTER SYSTEM WITH DETACHABLE DISPLAY	THAKKAR, SHREEKANT SURYAKANT
10367378	Not Issued	041	02/14/2003	CONVERTIBLE AND DETACHABLE LAPTOPS	THAKKAR, SHREEKANT SURYAKANT
10367364	Not Issued	030	02/14/2003	CONNECTION ARRANGEMENT FOR A PEN-BASED COMPUTING SYSTEM	THAKKAR, SHREEKANT SURYAKANT
10164976	Not Issued	095	06/06/2002	METHOD AND APPARATUS FOR STAGGERING EXECUTION OF A SINGLE PACKED DATA INSTRUCTION USING THE SAME CIRCUIT	THAKKAR, SHREEKANT S.
10164774	Not Issued	095	06/06/2002	METHOD AND APPARATUS FOR STAGGERING EXECUTION OF A SINGLE PACKED DATA INSTRUCTION USING THE SAME CIRCUIT	THAKKAR, SHREEKANT S.

10057704	Not Issued	030	01/24/2002	CONTEXT-BASED INFORMATION PROCESSING	THAKKAR, SHREEKANT S.
10057163	Not Issued	019	01/24/2002	CONTEXT-BASED INFORMATION PROCESSING	THAKKAR, SHREEKANT S.
09852217	Not Issued	092	05/08/2001	EXECUTING PARTIAL-WIDTH PACKED DATA INSTRUCTIONS	THAKKAR, SHREEKANT S.
09845610	6502115	150	04/27/2001	CONVERSION BETWEEN PACKED FLOATING POINT DATA AND PACKED 32-BIT INTEGER DATA IN DIFFERENT ARCHITECTURAL REGISTERS	THAKKAR, SHREEKANT S.
09844728	6480868	150	04/27/2001	CONVERSION FROM PACKED FLOATING POINT DATA TO PACKED 8-BIT INTEGER DATA IN DIFFERENT ARCHITECTURAL REGISTERS	THAKKAR, SHREEKANT S.
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	THAKKAR, SHREEKANT S.
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
09805280	6425073	150	03/13/2001	METHOD AND APPARATUS FOR STAGGERING EXECUTION OF AN INSTRUCTION	THAKKAR, SHREEKANT S.
09783875	Not Issued	160	02/14/2001	METHOD AND APPARATUS FOR FLOATING POINT OPERATIONS AND FORMAT CONVERSION OPERATIONS	THAKKAR, SHREEKANT S.
09783779	Not Issued	041	02/14/2001	SHUFFLE INSTRUCTIONS	THAKKAR, SHREEKANT S.
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
09748813	6622087	150	12/26/2000	METHOD AND APPARATUS FOR DERIVING TRAVEL PROFILES	THAKKAR, SHREEKANT
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.

09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	THAKKAR, SHREEKANT S.
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	THAKKAR, SHREEKANT S.
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
09608982	Not Issued	030	06/30/2000	DIGITAL WATERMARKS WITH VALUES DERIVED FROM REMOTE PLATFORMS	THAKKAR, SHREEKANT S.
09566620	Not Issued	041	05/08/2000	PROVIDING INFORMATION TO A COMMUNICATIONS DEVICE	THAKKAR, SHREEKANT S.
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	THAKKAR, SHREEKANT S.
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	THAKKAR, SHREEKANT S
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	THAKKAR, SHREEKANT S.
09540946	Not	030	03/31/2000	PROTECTING SOFTWARE	THAKKAR,

	Issued			ENVIRONMENT IN ISOLATED EXECUTION	SHREEKANT S.
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	THAKKAR, SHREEKANT S.
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	THAKKAR, SHREEKANT S.
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	THAKKAR, SHREEKANT S.
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	THAKKAR, SHREEKANT S.
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	THAKKAR, SHREEKANT S.
09223302	6463494	150	12/30/1998	METHOD AND SYSTEM FOR IMPLEMENTING CONTROL SIGNALS ON A LOW PIN COUNT BUS	THAKKAR, SHREEKANT
09176571	6434650	150	10/21/1998	APPARATUS AND METHOD FOR MULTIPLEXING BI-DIRECTIONAL DATA ONTO A LOW PIN COUNT BUS BETWEEN A HOST CPU AND A CO-PROCESSOR	THAKKAR, SHREEKANT
09170136	6185670	150	10/12/1998	SYSTEM FOR REDUCING NUMBER OF OPCODES	THAKKAR, SHREEKANT S.

				REQUIRED IN A PROCESSOR USING AN INSTRUCTION FORMAT INCLUDING OPERATION CLASS CODE AND OPERATION SELECTOR CODE FIELDS	
09122349	Not Issued	041	07/24/1998	METHOD AND APPARATUS FOR PERFORMING CACHE SEGMENT FLUSH AND CACHE SEGMENT INVALIDATION OPERATIONS	THAKKAR , SHREEKANT
09052881	6052769	150	03/31/1998	METHOD AND APPARATUS FOR MOVING SELECT NON-CONTIGUOUS BYTES OF PACKED DATA IN A SINGLE INSTRUCTION	THAKKAR , SHREEKANT
09049827	6317824	150	03/27/1998	METHOD AND APPARATUS FOR PERFORMING INTEGER OPERATIONS IN RESPONSE TO A RESULT OF A FLOATING POINT OPERATION	THAKKAR , SHREEKANT
08914578	5946713	150	08/18/1997	MEMORY ATTRIBUTE PALETTE	THAKKAR , SHREEKANT S.
08579932	5724527	150	12/28/1995	FAULT-TOLERANT BOOT STRAP MECHANISM FOR A MULTIPROCESSOR SYSTEM	THAKKAR , SHREEKANT

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Inventor Name Search Result

Your Search was:

Last Name = SUTTON
First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60491832	Not Issued	020	08/01/2003	ADAMANTYLGLYCINE-BASED INHIBITORS OF DIPEPTIDYL PEPTIDASE IV AND METHODS	SUTTON, JAMES C.
60434060	Not Issued	020	12/17/2002	BETA LACTAM COMPOUNDS AND THEIR USE AS INHIBITORS OF TRYPTASE	SUTTON, JAMES C.
60364596	Not Issued	159	03/18/2002	ELECTRONIC DOCUMENT PRESENTMENT SERVICES IN THE EVENT OF A DISASTER	SUTTON, JAMES N.
60361881	Not Issued	159	03/06/2002	PULSE & RESPIRATION TIMER	SUTTON, JAMES R.
60246392	Not Issued	159	11/07/2000	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
60246391	Not Issued	159	11/07/2000	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A
10321736	Not Issued	041	12/17/2002	HAND WINCH HANDLE WITH RELEASE FEATURE THAT PERMITS CABLE TO WIND OFF OF WINCH DRUM WITHOUT HANDLE ROTATION	SUTTON, JAMES A.
10165597	Not Issued	030	06/07/2002	SYSTEM AND METHOD FOR	SUTTON,

				PROTECTION AGAINST UNTRUSTED SYSTEM MANAGEMENT CODE BY REDIRECTING A SYSTEM MANAGEMENT INTERRUPT AND CREATING A VIRTUAL MACHINE CONTAINER	JAMES A.
[10123599](#)	Not Issued	030	04/15/2002	PROTECTION AGAINST MEMORY ATTACKS FOLLOWING RESET	SUTTON, JAMES A.
[10122785](#)	Not Issued	030	04/15/2002	VALIDATION OF INCLUSION OF A PLATFORM WITHIN A DATA CENTER	SUTTON, JAMES A.
[10122544](#)	Not Issued	030	04/15/2002	METHOD AND APPARATUS FOR COMMUNICATING SECURELY WITH A TOKEN	SUTTON, JAMES A.
[10121807](#)	Not Issued	030	04/12/2002	MICROCODE PATCH AUTHENTICATION	SUTTON, JAMES A.
[10116923](#)	Not Issued	030	04/05/2002	METHODS AND ARRANGEMENTS TO REGISTER CODE	SUTTON, JAMES A.
[10112169](#)	Not Issued	030	03/29/2002	SYSTEM AND METHOD FOR EXECUTION OF A SECURED ENVIRONMENT INITIALIZATION INSTRUCTION	SUTTON, JAMES A.
[10112124](#)	Not Issued	030	03/29/2002	SYSTEM AND METHOD FOR RESETTING A PLATFORM CONFIGURATION REGISTER	SUTTON, JAMES A.
[10085839](#)	Not Issued	030	02/25/2002	METHOD AND APPARATUS FOR LOADING A TRUSTABLE OPERATING SYSTEM	SUTTON, JAMES A.
[10052927](#)	[6642252](#)	150	11/07/2001	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
[10043843](#)	Not Issued	030	11/01/2001	APPARATUS AND METHOD FOR UNILATERALLY LOADING A SECURE OPERATING SYSTEM WITHIN A MULTIPROCESSOR ENVIRONMENT	SUTTON, JAMES A.
[10041071](#)	Not Issued	030	12/28/2001	AUTHENTICATED CODE METHOD AND APPARATUS	SUTTON, JAMES A.
[10040872](#)	[6646846](#)	150	10/29/2001	APPARATUS AND METHOD FOR STAGED POWER LOAD TRANSFER	SUTTON, JAMES A.
[10039961](#)	Not Issued	030	12/31/2001	PROCESSOR SUPPORTING EXECUTION OF AN AUTHENTICATED CODE	SUTTON, JAMES A.

				INSTRUCTION	
10039595	Not Issued	030	12/31/2001	AUTHENTICATED CODE MODULE	SUTTON, JAMES A.
10035714	Not Issued	164	11/07/2001	ACID DERIVATIVES USEFUL AS SERINE PROTEASE INHIBITORS	SUTTON, JAMES C.
09942503	Not Issued	030	08/29/2001	ACQUISITION AND ORGANIZATION OF DIGITAL IMAGES	SUTTON, JAMES E.
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	SUTTON, JAMES A.
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	SUTTON, JAMES A.
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	SUTTON, JAMES A.
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	SUTTON, JAMES A.
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	SUTTON, JAMES A.
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A	SUTTON, JAMES A.

				CLEANUP FLAG IS SET	
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
09605605	Not Issued	041	06/28/2000	PLATFORM AND METHOD FOR ESTABLISHING PROVABLE IDENTITIES WHILE MAINTAINING PRIVACY	SUTTON, JAMES A.
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	SUTTON, JAMES A.
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	SUTTON, JAMES A
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	SUTTON, JAMES A.
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	SUTTON, JAMES A.
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	SUTTON, JAMES A.
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	SUTTON, JAMES A.
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	SUTTON, JAMES A.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	SUTTON, JAMES A.
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	SUTTON, JAMES A.
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	SUTTON, JAMES A.
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR	SUTTON,

				ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	JAMES A.

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Your Search was:

Last Name = RENERIS
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Application#	Patent#	Status	Date Filed	Title	Inventor Name 39
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	RENERIS, KEN
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	RENERIS, KEN
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	RENERIS, KEN
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	RENERIS, KEN
09672367	Not Issued	030	09/28/2000	MECHANISM TO SECURE COMPUTER OUTPUT FROM SOFTWARE ATTACK USING ISOLATED EXECUTION	RENERIS, KEN
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	RENERIS, KEN

09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	RENERIS, KEN
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	RENERIS, KEN
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	RENERIS, KEN
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
09561213	Not Issued	161	04/28/2000	METHOD AND APPARATUS FOR REMOTELY DEBUGGING COMPUTER SOFTWARE OVER A SERIAL BUS	RENERIS, KENNETH S.
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	RENERIS, KEN
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	RENERIS, KEN
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	RENERIS, KEN
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	RENERIS, KEN
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	RENERIS, KEN
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	RENERIS, KEN

09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	RENERIS, KEN
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	RENERIS, KEN
09539588	6243821	150	03/31/2000	SYSTEM AND METHOD FOR MANAGING POWER CONSUMPTION IN A COMPUTER SYSTEM	RENERIS, KENNETH S.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	RENERIS, KEN
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	RENERIS, KEN
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	RENERIS, KEN
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	RENERIS, KEN
09488015	Not Issued	168	01/20/2000	METHOD AND APPARATUS FOR REMOTELY DEBUGGING COMPUTER SOFTWARE OVER A SERIAL BUS	RENERIS, KENNETH S.
09257923	6526431	150	02/26/1999	MAINTAINING EXTENDED AND TRADITIONAL STATES OF A PROCESSING UNIT IN TASK SWITCHING	RENERIS , KENNETH S.
09158167	6209088	150	09/21/1998	COMPUTER HIBERNATION IMPLEMENTED BY A COMPUTER OPERATING SYSTEM	RENERIS , KENNETH S.
09098925	6092208	150	06/17/1998	A SYSTEM AND METHOD FOR WAKING A COMPUTER HAVING A PLURALITY OF POWER RESOURCES FROM A SYSTEM STATE USING A DATA STRUCTURE	RENERIS , KENNETH S.
08953933	Not Issued	161	10/20/1997	OPERATING SYSTEM DEVICE CONFIGURATION MANAGER	RENERIS , KENNETH S.

				AND METHOD	
08831111	6343338	150	04/01/1997	SYSTEM AND METHOD FOR SYNCHRONIZING DISPARATE PROCESSING MODES AND FOR CONTROLLING ACCESS TO SHARED RESOURCES	RENERIS, KENNETH S.
08810019	5903894	150	03/03/1997	SYSTEM AND METHOD FOR USING A HIERARCHICAL DATA STRUCTURE TO CONTROL AND IDENTIFY DEVICES AND REPRESENT CONNECTIONS BETWEEN THE DEVICES	RENERIS, KENNETH S.
08810018	5919264	150	03/03/1997	A SYSTEM AND METHOD FOR USING DATA STRUCTURES TO SHARE A PLURALITY OF POWER RESOURCES AMONG A PLURALITY OF DEVICES	RENERIS, KENNETH S.
08614186	5784628	150	03/12/1996	METHOD AND SYSTEM FOR CONTROLLING POWER CONSUMPTION IN A COMPUTER SYSTEM	RENERIS, KENNETH S.
08428961	Not Issued	166	04/24/1995	OPERATING SYSTEM DEVICE CONFIGURATION MANAGER AND METHOD	RENERIS, KENNETH S.

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Inventor Name Search Result

Your Search was:

Last Name = NEIGER
First Name = GIL

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
10676890	Not Issued	019	09/30/2003	CHIPSET SUPPORT FOR MANAGING HARDWARE INTERRUPTS IN A VIRTUAL MACHINE SYSTEM	NEIGER, GILBERT
10676887	Not Issued	019	09/30/2003	MECHANISM TO CONTROL HARDWARE INTERRUPT ACKNOWLEDGEMENT IN A VIRTAL MACHINE SYSTEM	NEIGER, GILBERT
10676737	Not Issued	020	09/30/2003	METHOD FOR RESOLVING ADDRESS SPACE CONFLICTS BETWEEN A VIRTUAL MACHINE MONITOR AND A GUEST OPERATING SYSTEM	NEIGER, GILBERT
10676584	Not Issued	018	09/30/2003	INVALIDATING TRANSLATION LOOKASIDE BUFFER ENTRIES IN A VIRTUAL MACHINE (VM) SYSTEM	NEIGER, GILBERT
10663206	Not Issued	019	09/15/2003	USE OF MULTIPLE VIRTUAL MACHINE MONITORS TO HANDLE PRIVILEGED EVENTS	NEIGER, GILBERT
10663205	Not Issued	019	09/15/2003	VECTORING AN INTERRUPT OR EXCEPTION UPON RESUMING OPERATION OF A	NEIGER, GILBERT

				VIRTUAL MACHINE	
10663163	Not Issued	019	09/15/2003	OPTIMIZING PROCESSOR-MANAGED RESOURCES BASED ON THE BEHAVIOR OF A VIRTUAL MACHINE MONITOR	NEIGER, GILBERT
10322003	Not Issued	020	12/17/2002	METHODS AND SYSTEMS TO CONTROL VIRTUAL MACHINES	NEIGER, GILBERT
10319900	Not Issued	030	12/12/2002	RECLAIMING EXISTING FIELDS IN ADDRESS TRANSLATION DATA STRUCTURES TO EXTEND CONTROL OVER MEMORY ACCESES	NEIGER, GILBERT
10194822	Not Issued	030	07/12/2002	SYSTEM AND METHOD FOR BINDING VIRTUAL MACHINES TO HARDWARE CONTEXTS	NEIGER, GILBERT
10185123	Not Issued	030	06/27/2002	INITIALIZATION OF PROTECTED SYSTEM	NEIGER, GILBERT
10184798	Not Issued	030	06/29/2002	HANDLING FAULTS ASSOCIATED WITH OPERATION OF GUEST SOFTWARE IN THE VIRTUAL-MACHINE ARCHITECTURE	NEIGER, GILBERT
10184774	Not Issued	030	06/29/2002	CONTROL OVER FAULTS OCCURRING DURING THE OPERATION OF GUEST SOFTWARE IN THE VIRTUAL-MACHINE ARCHITECTURE	NEIGER, GILBERT
10165597	Not Issued	030	06/07/2002	SYSTEM AND METHOD FOR PROTECTION AGAINST UNTRUSTED SYSTEM MANAGEMENT CODE BY REDIRECTING A SYSTEM MANAGEMENT INTERRUPT AND CREATING A VIRTUAL MACHINE CONTAINER	NEIGER, GILBERT
10124641	Not Issued	020	04/16/2002	CONTROL REGISTER ACCESS VIRTUALIZATION PERFORMANCE IMPROVEMENT IN THE VIRTUAL-MACHINE ARCHITECTURE	NEIGER, GILBERT
10084282	Not Issued	030	02/25/2002	METHOD AND APPARATUS FOR TRANSLATING GUEST PHYSICAL ADDRESSES IN A	NEIGER, GILBERT

				VIRTUAL MACHINE ENVIRONMENT	
10043843	Not Issued	030	11/01/2001	APPARATUS AND METHOD FOR UNILATERALLY LOADING A SECURE OPERATING SYSTEM WITHIN A MULTIPROCESSOR ENVIRONMENT	NEIGER, GILBERT
10041071	Not Issued	030	12/28/2001	AUTHENTICATED CODE METHOD AND APPARATUS	NEIGER, GILBERT
10039961	Not Issued	030	12/31/2001	PROCESSOR SUPPORTING EXECUTION OF AN AUTHENTICATED CODE INSTRUCTION	NEIGER, GILBERT
10039595	Not Issued	030	12/31/2001	AUTHENTICATED CODE MODULE	NEIGER, GILBERT
09931072	Not Issued	030	08/15/2001	TRACKING OPERATING SYSTEM PROCESS AND THREAD EXECUTION AND VIRTUAL MACHINE EXECUTION IN HARDWARE OR IN A VIRTUAL MACHINE MONITOR	NEIGER, GILBERT
09823482	Not Issued	030	03/30/2001	METHOD AND APPARATUS FOR CONSTRUCTING HOST PROCESSOR SOFT DEVICES INDEPENDENT OF THE HOST PROCESSOR OPERATING SYSTEM	NEIGER, GILBERT
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	NEIGER, GILBERT
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	NEIGER, GILBERT
09752586	Not Issued	030	12/27/2000	MECHANISM FOR PROVIDING POWER MANAGEMENT THROUGH VIRTUALIZATION	NEIGER, GILBERT
09752134	Not Issued	030	12/27/2000	PROCESSOR MODE FOR LIMITING THE OPERATION OF GUEST SOFTWARE RUNNING ON A VIRTUAL MACHINE SUPPORTED BY A VIRTUAL MACHINE MONITOR	NEIGER, GILBERT
09752133	Not Issued	041	12/27/2000	VIRTUAL TRANSLATION LOOKASIDE BUFFER	NEIGER, GILBERT

09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GILBERT
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	NEIGER, GILBERT
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	NEIGER, GILBERT
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	NEIGER, GILBERT
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	NEIGER, GILBERT
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	NEIGER, GILBERT
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	NEIGER, GIL
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GIL
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	NEIGER, GIL
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	NEIGER, GIL
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	NEIGER, GILBERT
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED	NEIGER, GIL

				EXECUTION	
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	NEIGER, GIL
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	NEIGER, GIL
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	NEIGER, GIL
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	NEIGER, GIL
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	NEIGER, GILBERT
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	NEIGER, GIL
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	NEIGER, GILBERT
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	NEIGER, GILBERT

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Inventor Name Search Result

Your Search was:

Last Name = HERBERT
First Name = HOWARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name 38
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C
09995994	6629244	150	11/16/2001	PLATFORM AND METHOD FOR ASSURING INTEGRITY OF TRUSTED AGENT COMMUNICATIONS	HERBERT, HOWARD C.
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	HERBERT, HOWARD C.
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	HERBERT, HOWARD C.
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	HERBERT, HOWARD C.
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	HERBERT, HOWARD C.

09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	HERBERT, HOWARD C.
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
09607412	Not Issued	030	06/29/2000	METHOD FOR SECURE DISTRIBUTION AND CONFIGURATION OF ASYMMETRIC KEYING MATERIAL INTO SEMICONDUCTOR DEVICES	HERBERT, HOWARD C.
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	HERBERT, HOWARD C.
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	HERBERT, HOWARD C
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	HERBERT, HOWARD C.
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	HERBERT, HOWARD C.
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR	HERBERT,

				GENERATING AND UTILIZING A PROTECTED AUDIT LOG	HOWARD C.
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	HERBERT, HOWARD C.
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	HERBERT, HOWARD C.
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	HERBERT, HOWARD C.
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	HERBERT, HOWARD C.
09475806	Not Issued	041	12/30/1999	PLATFORM AND METHOD FOR PERFORMANCE OPTIMIZATION OF MAGIC COOKIE COMPUTATION	HERBERT, HOWARD C.
09298360	6389537	150	04/23/1999	PLATFORM AND METHOD FOR ASSURING INTEGRITY OF TRUSTED AGENT COMMUNICATIONS	HERBERT , HOWARD C.
09287782	6199053	150	04/08/1999	DIGITAL SIGNATURE PURPOSE ENCODING	HERBERT , HOWARD C.
09119682	Not Issued	161	07/21/1998	TRANSFORMER	HERBERT , HOWARD
09071374	Not Issued	093	04/30/1998	CRYPTOGRAPHICALLY PROTECTED PAGING SUBSYSTEM	HERBERT , HOWARD C.
08764154	5757919	150	12/12/1996	CRYPTOGRAPHICALLY PROTECTED PAGING SUBSYSTEM	HERBERT , HOWARD C.
08720444	6023509	150	09/30/1996	DIGITAL SIGNATURE PURPOSE ENCODING	HERBERT , HOWARD C.
08631256	5686677	150	04/12/1996	ACOUSTIC GUITAR BRIDGE SUPPORT	HERBERT , HOWARD

08625537	5883956	150	03/28/1996	DYNAMIC CONFIGURATION OF A SECURE PROCESSING UNIT FOR OPERATIONS IN VARIOUS ENVIRONMENTS	HERBERT, HOWARD C.
08485816	Not Issued	161	06/07/1995	CRYPTOGRAPHIC PROCESSOR WITH MULTIPLE KEY PAIRS FOR USER AND DEVICE AUTHENTICATION	HERBERT, HOWARD C.

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Inventor Name Search Result

Your Search was:

Last Name = GOLLIVER
First Name = ROGER

Application#	Patent#	Status	Date Filed	Title	Inventor Name 45
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A
10055406	Not Issued	168	11/07/2001	COMPUTER PRODUCT AND METHOD FOR SPARSE MATRICES	GOLLIVER, ROGER
09874865	6502117	150	06/04/2001	DATA MANIPULATION INSTRUCTION FOR ENHANCING VALUE AND EFFICIENCY OF COMPLEX ARITHMETIC	GOLLIVER, ROGER A.
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	GOLLIVER, ROGER A.
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	GOLLIVER, ROGER A.
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.

09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	GOLLIVER, ROGER A.
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	GOLLIVER, ROGER A.
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	GOLLIVER, ROGER A.
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	GOLLIVER, ROGER A.
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	GOLLIVER, ROGER A
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	GOLLIVER, ROGER A.
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	GOLLIVER, ROGER A.
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND	GOLLIVER, ROGER A.

				UTILIZING A PROTECTED AUDIT LOG	
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	GOLLIVER, ROGER A.
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	GOLLIVER, ROGER A.
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	GOLLIVER, ROGER A.
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	GOLLIVER, ROGER A.
09223457	6321327	150	12/30/1998	METHOD FOR SETTING A BIT ASSOCIATED WITH EACH COMPONENT OF PACKED FLOATING-PINT OPERAND THAT IS NORMALIZED IN SIMD OPERATIONS	GOLLIVER, ROGER A.
09188868	6105047	150	11/09/1998	METHOD AND APPARATUS FOR TRADING PERFORMANCE FOR PRECISION WHEN PROCESSING DENORMAL NUMBERS IN A COMPUTER SYSTEM	GOLLIVER, ROGER
09183065	6243734	150	10/30/1998	COMPUTER PRODUCT AND METHOD FOR SPARSE MATRICES	GOLLIVER, ROGER
09170473	6272512	150	10/12/1998	DATA MANIPULATION INSTRUCTION FOR ENHANCING VALUE AND EFFICIENCY OF COMPLEX ARITHMETIC	GOLLIVER, ROGER A.
09170472	Not Issued	161	10/12/1998	CONDITIONAL OPERAND	GOLLIVER,

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Inventor Name Search Result

Your Search was:

Last Name = ELLISON
First Name = CARL

Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
60489860	Not Issued	020	07/23/2003	SIMPLIFIED LIVENESS PROTOCOL	ELLISON, CARL M.
60467294	Not Issued	020	05/01/2003	LIVENESS PROTOCOL	ELLISON, CARL M.
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M
60160719	Not Issued	159	10/21/1999	REAL-TIME CYTOMETRY SIGNAL TRANSFORMATION SYSTEM	ELLISON , CARL E.
10678745	Not Issued	019	10/03/2003	INTEGRATING A DEVICE INTO A SECURE NETWORK	ELLISON, CARL M.
10608326	Not Issued	020	06/27/2003	METHODS AND APPARATUS TO PROVIDE SECURE FIRMWARE STORAGE AND SERVICE ACCESS	ELLISON, CARL M.
10335058	Not Issued	020	12/31/2002	APPARATUS AND ASSOCIATED METHODS FOR THE SYNCHRONIZATION OF SHARED CONTENT	ELLISON, CARL
10316595	Not Issued	030	12/10/2002	PUBLIC KEY MEDIA KEY BLOCK	ELLISON, CARL M.
10210341	Not Issued	030	07/31/2002	SENSORY VERIFICATION OF SHARED DATA	ELLISON, CARL M.
10111026	Not Issued	030	04/18/2002	TRANSIENTLY DYNAMIC	ELLISON,

				FLOW CYTOMETER ANALYSIS SYSTEM	CARL E
09961380	Not Issued	030	09/25/2001	AUTHENTICATED PUBLIC KEY TRANSMISSION	ELLISON, CARL M.
09896088	Not Issued	030	06/28/2001	TIME VARYING PRESENTATION OF ITEMS BASED ON A KEY HASH	ELLISON, CARL M.
09895370	Not Issued	030	06/29/2001	METHOD AND APPARATUS FOR AUTHENTICATING PEOPLE USING BUSINESS CARDS	ELLISON, CARL M.
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	ELLISON, CARL M.
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	ELLISON, CARL M.
09784941	Not Issued	030	02/15/2001	METHOD OF ENFORCING AUTHORIZATION IN SHARED PROCESSES USING ELECTRONIC CONTRACTS	ELLISON, CARL M.
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	ELLISON, CARL M.
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	ELLISON, CARL M.
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	ELLISON, CARL M.
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	ELLISON, CARL M.
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	ELLISON, CARL M.
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.

09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	ELLISON, CARL M.
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
09605605	Not Issued	041	06/28/2000	PLATFORM AND METHOD FOR ESTABLISHING PROVABLE IDENTITIES WHILE MAINTAINING PRIVACY	ELLISON, CARL M.
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	ELLISON, CARL M.
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	ELLISON, CARL M
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	ELLISON, CARL M.
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	ELLISON, CARL M.
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	ELLISON, CARL M.
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	ELLISON, CARL M.
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	ELLISON, CARL M.
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	ELLISON, CARL M.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	ELLISON, CARL M.
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A	ELLISON, CARL M.

				HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	ELLISON, CARL M.
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	ELLISON, CARL M.
09481747	Not Issued	093	01/11/2000	SYSTEM FOR PROTECTING ANONYMITY OF PARTIES INVOLVED IN A PERSON-TO-PERSON ELECTRONIC COMMERCE TRANSACTION	ELLISON, CARL M.
09452329	Not Issued	041	11/30/1999	STREAM CIPHER HAVING A COMBINER FUNCTION WITH STORAGE BASED SHUFFLE UNIT	ELLISON, CARL M.
09062748	5991406	150	04/20/1998	SYSTEM AND METHOD FOR DATA RECOVERY	ELLISON, CARL M.
08874459	5956403	150	06/16/1997	SYSTEM AND METHOD FOR KEY ESCROW ENCRYPTION	ELLISON, CARL M.
08781626	5745573	150	01/10/1997	SYSTEM AND METHOD FOR CONTROLLING ACCESS TO A USER SECRET	ELLISON, CARL M.
08715377	5640454	150	09/12/1996	SYSTEM AND METHOD FOR ACCESS FIELD VERIFICATION	ELLISON, CARL M.
08691564	Not Issued	166	08/02/1996	SYSTEM AND METHOD FOR CONTROLLING ACCESS TO A USER SECRET	ELLISON, CARL M.
08390959	5557765	150	02/21/1995	SYSTEM AND METHOD FOR DATA RECOVERY	ELLISON, CARL M.
08289602	5557346	150	08/11/1994	SYSTEM AND METHOD FOR KEY ESCROW ENCRYPTION	ELLISON, CARL M.
07898157	5475860	150	06/15/1992	INPUT/OUTPUT CONTROL SYSTEM AND METHOD FOR DIRECT MEMORY TRANSFER ACCORDING TO LOCATION ADDRESSES PROVIDED BY THE SOURCE UNIT AND DESTINATION ADDRESSES PROVIDED BY THE DESTINATION UNIT	ELLISON, CARL

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Last Name = MITTAL
First Name = MILLIND

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60437944	Not Issued	020	01/02/2003	STORAGE ACCELERATION AND VIRTUALIZATION INTERFACE CARD (SAVIC) FOR SERVERS FOR IMPROVED PERFORMANCE AND SCALABILITY OF STORAGE SUB-SYSTEM	MITTAL, MILLIND
60437809	Not Issued	020	01/02/2003	NETWORKING ACCELERATION AND VIRTUALIZATION INTERFACE CARD (NAVIC) FOR SERVERS FOR IMPROVED PERFORMANCE AND SCALABILITY	MITTAL, MILLIND
60320216	Not Issued	020	05/23/2003	FIELD-PROGRAMMABLE EXTENDED FUNCTION-LEVEL-INSTRUCTION-SET COMPUTER ARCHITECTURE	MITTAL, MILLIND
60249727	Not Issued	159	11/17/2000	METHOD AND APPARATUS FOR PERSONAL AUTOMATIC FILING SYSTEM	MITTAL, MILLIND
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
10623062	Not Issued	020	07/18/2003	METHOD AND APPARATUS FOR PERFORMING PACKED SHIFT OPERATIONS	MITTAL, MILLIND
10611621	Not Issued	019	06/30/2003	METHOD AND APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	MITTAL, MILLIND
10461782	Not Issued	019	06/12/2003	METHOD FOR PROCESSING BRANCH OPERATIONS	MITTAL, MILLIND
10352800	Not Issued	020	01/27/2003	NETWORKED APPLICATION REQUEST SERVICING OFFLOADED FROM HOST	MITTAL, MILLIND
10249416	Not Issued	030	04/07/2003	NATIVE COPY INSTRUCTION FOR FILE-ACCESS PROCESSOR WITH	MITTAL, MILLIND

				COPY-RULE-BASED VALIDATION	
10249359	Not Issued	030	04/02/2003	NATIVE LOOKUP INSTRUCTION FOR FILE-ACCESS PROCESSOR SEARCHING A THREE-LEVEL LOOKUP CACHE FOR VARIABLE-LENGTH KEYS	MITTAL, MILLIND
10248029	Not Issued	030	12/12/2002	FUNCTIONAL-LEVEL INSTRUCTION-SET COMPUTER ARCHITECTURE FOR PROCESSING APPLICATION-LAYER CONTENT-SERVICE REQUESTS SUCH AS FILE-ACCESS REQUESTS	MITTAL, MILLIND
10185896	Not Issued	030	06/27/2002	METHOD AND APPARATUS FOR UNPACKING PACKED DATA	MITTAL, MILLIND
10184197	Not Issued	041	06/27/2002	METHOD AND APPARATUS FOR PACKING DATA	MITTAL, MILLIND
10027939	Not Issued	030	12/20/2001	METHOD AND APPARATUS FOR ENABLING A LOW POWER MODE FOR A PROCESSOR	MITTAL, MILLIND
09989736	Not Issued	030	11/19/2001	METHOD AND APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	MITTAL, MILLIND
09785303	Not Issued	093	02/16/2001	METHODS AND APPARATUSES TO CLEAR STATE FOR OPERATION OF A STACK	MITTAL, MILLIND
09783816	Not Issued	093	01/14/2001	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT OPERATIONS	MITTAL, MILLIND
09770832	Not Issued	030	01/25/2001	METHOD AND APPARATUS FOR END TO END FORWARDING ARCHITECTURE	MITTAL, MILLIND
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
09747122	6631389	150	12/22/2000	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT OPERATIONS	MITTAL, MILLIND
09737657	Not Issued	030	12/13/2000	METHOD AND APPARATUS FOR FLEXIBLE LAYER ENCAPSULATION	MITTAL, MILLIND
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MITTAL, MILLIND
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MITTAL, MILLIND
09668610	Not	030	09/22/2000	PROTECTING SOFTWARE	MITTAL,

	Issued			ENVIRONMENT IN ISOLATED EXECUTION	MILLIND
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	MITTAL, MILLIND
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MITTAL, MILLIND
09657448	6516406	150	09/08/2000	PROCESSOR EXECUTING UNPACK INSTRUCTION TO INTERLEAVE DATA ELEMENTS FROM TWO PACKED DATA	MITTAL, MILLIND
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	MITTAL, MILLIND
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
09608439	Not Issued	030	06/30/2000	METHOD AND APPARATUS FOR SECURE EXECUTION USING A SECURE MEMORY PARTITION	MITTAL, MILLIND
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MITTAL, MILLIND
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	MITTAL, MILLIND
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	MITTAL, MILLIND
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	MITTAL, MILLIND
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MITTAL, MILLIND
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	MITTAL, MILLIND

09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES ● A PROCESSOR FOR ISOLATED EXECUTION	MITTAL, MILLIND
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MITTAL, MILLIND
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIERARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	MITTAL, MILLIND
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	MITTAL, MILLIND
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	MITTAL, MILLIND
09525702	Not Issued	041	03/14/2000	METHOD AND APPARATUS FOR HARDWARE PLATFORM IDENTIFICATION WITH PRIVACY PROTECTION	MITTAL, MILLIND
08960413	5983256	150	10/29/1997	APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	MITTAL , MILLIND
08897283	5819101	150	07/21/1997	METHOD FOR PACKING A PLURALITY OF PACKED DATA ELEMENTS IN RESPONSE TO A PACK INSTRUCTION	MITTAL , MILLIND
08699993	5859997	150	08/20/1996	METHOD FOR PERFORMING MULTIPLY-SUBSTRACT OPERATIONS ON PACKED DATA	MITTAL , MILLIND
08630876	5677862	150	04/02/1996	A METHOD FOR MULTIPLYING PACKED DATA	MITTAL , MILLIND
08626698	Not Issued	166	04/02/1996	A METHOD FOR PACKING AND UNPACKING PACKED DATA	MITTAL , MILLIND
08176123	Not Issued	161	12/30/1993	METHOD AND APPARATUS USING PACKED DATA IN A PROCESSOR	MITTAL , MILLIND
08175772	Not Issued	166	12/30/1993	METHOD AND APPARATUS USING NOVEL OPERATIONS IN A PROCESSOR	MITTAL , MILLIND

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Your Search was:

Last Name = LIN
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Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C
60197152	Not Issued	159	04/13/2000	CRYPTOGRAPHY PROCESSING UNIT	LIN, DERRICK C.
10663247	Not Issued	020	09/15/2003	REGISTER STACK ENGINE HAVING SPECULATIVE LOAD/STORE MODES	LIN, DERRICK
10623062	Not Issued	020	07/18/2003	METHOD AND APPARATUS FOR PERFORMING PACKED SHIFT OPERATIONS	LIN, DERRICK CHU
10218206	Not Issued	120	08/12/2002	CLASSIFICATION ENGINE IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK C.
10218159	Not Issued	120	08/12/2002	DISTRIBUTED PROCESSING IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK C.
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	LIN, DERRICK C.
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	LIN, DERRICK C.
09783816	Not Issued	093	01/14/2001	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT	LIN, DERRICK CHU

				OPERATIONS	
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
09747122	6631389	150	12/22/2000	PROCESSOR CAPABLE OF EXECUTING PACKED SHIFT OPERATIONS	LIN, DERRICK CHU
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	LIN, DERRICK C.
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	LIN, DERRICK C.
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	LIN, DERRICK C.
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	LIN, DERRICK C.
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
09610798	Not Issued	030	07/06/2000	DISTRIBUTED PROCESSING IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK
09610722	Not Issued	030	07/06/2000	CLASSIFICATION ENGINE IN A CRYPTOGRAPHY ACCELERATION CHIP	LIN, DERRICK C.
09561145	6631452	150	04/28/2000	REGISTER STACK ENGINE	LIN, DERRICK

				HAVING SPECULATIVE LOAD/STORE MODES	
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	LIN, DERRICK C.
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	LIN, DERRICK C
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	LIN, DERRICK C.
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	LIN, DERRICK C.
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	LIN, DERRICK C.
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	LIN, DERRICK C.
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	LIN, DERRICK C.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	LIN, DERRICK C.
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	LIN, DERRICK C.
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	LIN, DERRICK C.
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	LIN, DERRICK C.
09474725	Not Issued	161	12/29/1999	EAGER REGISTER STACK ENGINE	LIN, DERRICK
09231426	6205542	150	01/14/1999	PROCESSOR PIPELINE	LIN,

				INCLUDING REPLAY	DERRICK C.
09132043	6035389	150	08/11/1998	SCHEDULING INSTRUCTIONS WITH DIFFERENT LATENCIES	LIN , DERRICK C.
08998341	6076153	150	12/24/1997	PROCESSOR PIPELINE INCLUDING PARTIAL REPLAY	LIN , DERRICK C.
08985615	5974525	150	12/05/1997	APPARATUS AND METHOD FOR ALLOWING MULTIPLE INSTRUCTIONS TO USE THE SAME LOGICAL REGISTERS BY REMAPPING THEM TO SEPARATE PHYSICAL SEGMENT REGISTERS WHEN THE FIRST IS BEING UTILIZED	LIN , DERRICK
08898720	6170997	150	07/22/1997	METHOD FOR EXECUTING INSTRUCTIONS THAT OPERATE ON DIFFERENT DATA TYPES STORED IN THE SAME SINGLE LOGICAL REGISTER FILE	LIN , DERRICK
08756074	5881279	150	11/25/1996	METHOD AND APPARATUS FOR HANDLING INVALID OPCODE FAULTS VIA EXECUTION OF AN EVENT-SIGNALING MIRRO-OPERATION	LIN , DERRICK C.
08724172	5991884	150	09/30/1996	METHOD FOR REDUCING PEAK POWER IN DISPATCHING INSTRUCTIONS TO MULTIPLE EXECUTION UNITS	LIN , DERRICK C.
08611123	5835782	150	03/04/1996	PACKED/ADD AND PACKED SUBTRACT OPERATIONS	LIN , DERRICK C.
08610495	6275834	150	03/04/1996	APPARATUS FOR PERFORMING PACKED SHIFT OPERATIONS	LIN , DERRICK C.
08606328	5959636	150	02/23/1996	METHOD AND APPARATUS FOR PERFORMING SATURATION INSTRUCTIONS USING SATURATION LIMIT VALUES	LIN , DERRICK C.
08606212	6035316	150	02/23/1996	APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA	LIN , DERRICK C.
08575704	5835748	150	12/19/1995	METHOD FOR EXECUTING DIFFERENT SETS OF INSTRUCTIONS THAT CAUSE A PROCESSOR TO PERFORM DIFFERENT DATA TYPE OPERATIONS ON DIFFERENT	LIN , DERRICK

				PHYSICAL REGISTER FILES THAT LOGICALLY APPEAR TO SOFTWARE AS A SINGLE ALIASED REGISTER FILE	
08574719	5852726	150	12/19/1995	METHOD AND APPARATUS FOR EXECUTING TWO TYPES OF INSTRUCTIONS THAT SPECIFY REGISTERS OF A SHARED LOGICAL REGISTER FILE IN A STACK AND NON-STACK REFERENCED MANNER	LIN, DERRICK
08574500	5701508	150	12/19/1995	EXECUTING DIFFERENT INSTRUCTIONS THAT CAUSE DIFFERENT DATA TYPE OPERATIONS TO BE PERFORMED ON SINGLE LOGICAL REGISTER FILE	LIN, DERRICK

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Last Name = MCKEEN
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60198227	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
60198226	Not Issued	159	03/31/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
60198225	Not Issued	159	03/31/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X
10676310	Not Issued	019	09/30/2003	METHOD AND SYSTEM FOR MULTIPLE BRANCH PATHS IN A MICROPROCESSOR	MCKEEN, FRANCIS
10644399	Not Issued	019	08/19/2003	METHOD AND APPARATUS TO RETAIN SYSTEM CONTROL WHEN A BUFFER OVERFLOW ATTACK OCCURS	MCKEEN, FRANCIS X.
10639060	Not Issued	019	08/11/2003	METHOD AND SYSTEM FOR AUTHENTICATING A USER OF A COMPUTER SYSTEM THAT HAS A TRUSTED PLATFORM MODULE (TPM)	MCKEEN, FRANCIS X.
09823131	Not Issued	030	03/30/2001	FILE CHECKING USING REMOTE SIGNING AUTHORITY VIA A NETWORK	MCKEEN, FRANCIS X.
09822986	Not Issued	030	03/30/2001	CHECKING FILE INTEGRITY USING SIGNATURE GENERATED IN ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09751586	Not Issued	051	12/29/2000	RESETTING A PROCESSOR IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
09705678	Not Issued	041	11/02/2000	RESCHEDULING MULTIPLE	MCKEEN,

				MICRO-OPERATIONS IN A PROCESSOR USING A REPLAY QUEUE	FRANCIS X.
09705668	Not Issued	041	11/02/2000	BREAKING REPLAY DEPENDENCY LOOPS IN A PROCESSOR USING A RESCHEDULED REPLAY QUEUE	MCKEEN, FRANCIS X.
09672603	Not Issued	093	09/29/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09672602	Not Issued	030	09/29/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MCKEEN, FRANCIS X.
09672368	Not Issued	030	09/28/2000	MECHANISM TO HANDLE EVENTS IN A MACHINE WITH ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09672367	Not Issued	030	09/28/2000	MECHANISM TO SECURE COMPUTER OUTPUT FROM SOFTWARE ATTACK USING ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09668610	Not Issued	030	09/22/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09668585	Not Issued	030	09/22/2000	MANAGING A SECURE PLATFORM USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
09668408	Not Issued	030	09/22/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
09618738	6678825	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE ISOLATED MEMORIES IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
09618659	Not Issued	093	07/18/2000	A METHOD AND SYSTEM FOR SCRUBBING AN ISOLATED AREA OF MEMORY AFTER RESET OF A PROCESSOR OPERATING IN ISOLATED EXECUTION MODE IF A CLEANUP FLAG IS SET	MCKEEN, FRANCIS X.
09618489	6633963	150	07/18/2000	CONTROLLING ACCESS TO MULTIPLE MEMORY ZONES IN AN ISOLATED EXECUTION	MCKEEN, FRANCIS X.

				ENVIRONMENT	
09541667	Not Issued	041	03/31/2000	ATTESTATION KEY MEMORY DEVICE AND BUS	MCKEEN, FRANCIS X.
09541477	6507904	150	03/31/2000	EXECUTING ISOLATED MODE IN A SECURE SYSTEM RUNNING IN PRIVILEGE RINGS	MCKEEN, FRANCIS X
09541108	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR REMOTE ATTESTATION OF A PLATFORM	MCKEEN, FRANCIS X.
09540946	Not Issued	030	03/31/2000	PROTECTING SOFTWARE ENVIRONMENT IN ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09540613	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
09540612	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR GENERATING AND UTILIZING A PROTECTED AUDIT LOG	MCKEEN, FRANCIS X.
09540611	Not Issued	030	03/31/2000	MANAGING ACCESSES IN A PROCESSOR FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09540608	Not Issued	161	03/31/2000	CONTROLLING ACCESSES TO ISOLATED MEMORY USING A MEMORY CONTROLLER FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09539348	Not Issued	071	03/31/2000	GENERATING A KEY HIEARARCHY FOR USE IN AN ISOLATED EXECUTION ENVIRONMENT	MCKEEN, FRANCIS X.
09539344	Not Issued	030	03/31/2000	MANAGING A SECURE ENVIROMENT USING A HIERARCHICAL EXECUTIVE ARCHITECTURE IN ISOLATED EXECUTION MODE	MCKEEN, FRANCIS X.
09538954	Not Issued	030	03/31/2000	GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION	MCKEEN, FRANCIS X.
09538951	Not Issued	030	03/31/2000	PLATFORM AND METHOD FOR ISSUING AND CERTIFYING A HARDWARE-PROTECTED ATTESTATION KEY	MCKEEN, FRANCIS X.
08752729	Not Issued	093	11/19/1996	MECHANISM FOR EXECUTING COMPUTER INSTRUCTIONS IN PARALLEL	MCKEEN , FRANCIS X.
08339628	Not Issued	166	11/15/1994	MECHANISM FOR EXECUTING	MCKEEN ,

				COMPUTER INSTRUCTIONS IN PARALLEL	FRANCIS X.
08079498	5428807	150	06/17/1993	A METHOD AND APPARATUS FOR PROPAGATING EXCEPTION CONDITIONS OF A COMPUTER SYSTEM	MCKEEN , FRANCIS X.
08079494	5420990	150	06/17/1993	MECHANISM FOR ENFORCING THE CORRECT ORDER OF INSTRUCTION EXECUTION	MCKEEN , FRANCIS X.
08079479	Not Issued	166	06/17/1993	MECHANISM FOR EXECUTING COMPUTER INSTRUCTIONS IN PARALLEL	MCKEEN , FRANCIS X.
08079309	5421022	150	06/17/1993	AN APPARATUS AND METHOD FOR SPECULATIVELY EXECUTING INSTRUCTIONS IN A COMPUTER SYSTEM	MCKEEN , FRANCIS X.
08072632	6154828	150	06/03/1993	METHOD AND APPARATUS FOR EMPLOYING A CYCLE BIT TO INDICATE COMPLETION OF PARALLEL EXECUTING INSTRUCTIONS	MCKEEN , FRANCIS X.
08061273	5285323	150	05/13/1993	INTEGRATED CIRCUIT CHIP HAVING PRIMARY AND SECONDARY RANDOM ACCESS MEMORIES FOR A HIERARCHICAL CACHE	MCKEEN , FRANCIS X.
07638149	5113521	150	01/09/1991	METHOD AND APPARATUS FOR HANDLING FAULTS OF VECTOR INSTRUCTIONS CAUSING MEMORY MANAGEMENT EXCEPTIONS	MCKEEN , FRANCIS X.
07093499	4980817	150	08/31/1987	VECTOR REGISTER SYSTEM FOR EXECUTING PLURAL READ/WRITE COMMANDS CONCURRENTLY & INDEPENDENTLY ROUTING DATA TO PLURAL READ/WRITE PORTS	MCKEEN , FRANCIS X.

Inventor Search Completed: No Records to Display.

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L Number	Hits	Search Text	DB	Time stamp
1	253	(("6505279") or ("5079737") or ("5628022") or ("6192455") or ("6085296") or ("6148379") or ("6499123") or ("6339816") or ("6125430") or ("5852717") or ("5386552") or ("4037214") or ("6249872") or ("6055637") or ("5757919") or ("5737760") or ("6292874") or ("6272533") or ("6339815") or ("5872994") or ("6301646") or ("6321314") or ("20010027527") or ("EP930567A") or ("WO9834365A") or ("4307214") or ("4366537") or ("5075842") or ("5890189") or ("5987557") or ("5255379") or ("5459869") or ("5615263") or ("5717903") or ("5764969") or ("2001003745") or ("4278837") or ("4521852") or ("5022077") or ("5293424") or ("5421006") or ("5473692") or ("5479509") or ("5568552") or ("5657445") or ("5729760") or ("5796845") or ("5805712") or ("5835594") or ("5844986") or ("5937063") or ("5978481") or ("6014745") or ("6058478") or ("6158546") or ("6175925") or ("4319323") or ("4759064") or ("4795893") or ("5007082") or ("5293424") or ("5317705") or ("5459867") or ("5504922") or ("5606617") or ("5901225") or ("5970147") or ("6061794") or ("6092095") or ("6115816") or ("6212635") or ("6222923") or ("6269392") or ("6330670") or ("6357004") or ("6363485") or ("6374286") or ("6378072") or ("6389537") or ("6412035") or ("6421702") or ("6445797") or ("6463537") or ("6535988") or ("6557104") or ("6633981") or ("DE4217444") or ("EP473913") or ("4571672") or ("5455909") or ("5953502") or ("6101584") or ("6633963") or ("6292569") or ("6507904") or ("6289462") or ("6157721") or ("5421006") or ("5361359") or ("5657445") or ("5493279") or ("6088451") or ("6253193") or ("6332135") or ("6336105") or ("6338050") or ("6363488") or ("6389402") or ("6427140") or ("6182227") or ("5185692") or ("5542044") or ("5276735") or ("5499297") or ("5502766") or ("5533123") or ("6138239") or ("6233576") or ("4926476") or ("6192512") or ("5892903") or ("6272533") or ("6430561") or ("6453345") or ("6405318") or ("5648648") or ("5910988") or ("6032137") or ("4624600") or ("5392357") or ("6038551") or ("6070242") or ("6266413")).PN. or ((2001/0021969) or (WO97/29567) or (WO01/75565) or (WO01/27723) or (WO01/27527)).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 13:52

| 2 | 263 | (("6289455") or ("6477651") or ("4432057") or ("5535386") or ("5796999") or ("5893117") or ("6321263") or ("4941090") or ("5202826") or ("5287268") or ("5291003") or ("5764742") or ("5796832") or ("5870733") or ("RE36116") or ("6002605") or ("6005939") or ("6128602") or ("6137710") or ("6308277") or ("6611810") or ("6067618") or ("5280619") or ("5534855") or ("5751956") or ("5801364") or ("5812991") or ("5844218") or ("5870546") or ("6023684") or ("6052781") or ("6061057") or ("6134591") or ("6141006") or ("6189030") or ("6385724") or ("6466966") or ("6477559") or ("5428795") or ("5483649") or ("4423287") or ("6134661") or ("5301231") or ("5343529") or ("6078908") or ("6332193") or ("6468160") or ("5265163") or ("5289540") or ("5434562") or ("5475839") or ("6327579") or ("6411943") or ("5504816") or ("5606507") or ("5666284") or ("5802175") or ("5812763") or ("5825893") or ("5898835") or ("5930774") or ("6292830") or ("6304967") or ("6393569") or ("6073118") or ("4627183") or ("5490097") or ("6192477") or ("6158007") or ("5191522") or ("5581764") or ("5765153") or ("5956408") or ("6292889") or ("6314409") or ("5289785") or ("5629679") or ("5898830") or ("5944824") or ("6052788") or ("6061449") or ("6061449") or ("6173402") or ("6247641") or ("6301660") or ("5850516") or ("5991401") or ("5491752") or ("5940799") or ("5347580") or ("5748741") or ("5815573") or ("5864683") or ("5867647") or ("5950195") or ("5968176") or ("5983350") or ("6058482") or ("6088452") or ("6138162") or ("6148387") or ("6173311") or ("6182226") or ("H001944") or ("6212558") or ("6212558") or ("6249866") or ("6298445") or ("6304973") or ("6321337") or ("6343362") or ("6442696") or ("5493668") or ("4868376") or ("6240466") or ("6367013") or ("6385645") or ("6442571") or ("5835764") or ("6490679") or ("5905861") or ("5311591") or ("5347578") or ("5412717") or ("5511184") or ("5815571") or ("6044467") or ("5907621") or ("6216231") or ("6216231") or ("5414833") or ("4821862") or ("5983348") or ("6125447") or ("6138238") or ("6199181") or ("6272641") or ("6144959")).PN. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/01/09 13:52 |

3	242	(("6006328") or ("6105137") or ("6173310") or ("6199049") or ("5025373") or ("5276878") or ("5563393") or ("5701473") or ("5745915") or ("5758149") or ("5862316") or ("5878416") or ("5893076") or ("5907801") or ("5940807") or ("5978577") or ("6014647") or ("6045039") or ("6049779") or ("6073140") or ("6076090") or ("6145738") or ("6149056") or ("6192358") or ("6260050") or ("6266651") or ("6269393") or ("6286756") or ("6320956") or ("6336137") or ("6601040") or ("6609103") or ("6622124") or ("4328544") or ("5920863") or ("5754772") or ("4274139") or ("4785408") or ("4799156") or ("4903200") or ("4969183") or ("5367639") or ("5577105") or ("5724556") or ("5761281") or ("5806074") or ("5826267") or ("5841842") or ("5872971") or ("5897634") or ("5999942") or ("6070190") or ("6070177") or ("6119104") or ("6125391") or ("6128611") or ("6141660") or ("6141699") or ("6208992") or ("6226675") or ("6226675") or ("6272675") or ("6360229") or ("6401111") or ("6446045") or ("6473807") or ("6625602") or ("5404508") or ("5629981") or ("3798605") or ("4251009") or ("4295039") or ("4351247") or ("4380201") or ("4386266") or ("4407146") or ("4616852") or ("4864616") or ("5001752") or ("5205224") or ("5267314") or ("5317137") or ("5473144") or ("5524073") or ("5548106") or ("5555303") or ("5613007") or ("5646998") or ("5710889") or ("5726430") or ("5732878") or ("5742034") or ("5793302") or ("5794221") or ("5819263") or ("5824571") or ("5842043") or ("5870721") or ("5883371") or ("5884312") or ("5890140") or ("5915008") or ("5923759") or ("5936541") or ("5963647") or ("5974148") or ("5970888") or ("6012067") or ("6018724") or ("6058378") or ("6064988") or ("6097306") or ("6098053") or ("6105131") or ("6141752") or ("6148405") or ("6188994") or ("6202159") or ("6230148") or ("6269348") or ("6283366")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 13:53
5	3474037	secur$5 or protect$ or integrit$ or guard$ or valid$	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/01/09 14:03
6	7577342	transaction$ or access$5 or exchang$ or input$ or output$ or translation$ or entry or enter$ or exit$ or enabl$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 13:57
7	8968022	information or parameter or detail or mode or type or number or minimum or maximum or limit	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/01/09 13:57
9	4346099	stor$5 or cach$5 or memory or register$ or spreadsheet$ or databas$ or buffer$ or bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 13:59

10	5583081	isolated or external or normal or logical or state or separate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:00
11	4147194	processor or microprocessor or circuit or micro or EPROM or PROM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:00
12	8347371	configuration$ or set$4 or architectur$ or structur$ or design$ or count$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:00
13	3158488	detector$ or sensor$ or monitor$ or check$ or snoop$ or manager$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:01
14	6610899	execution$ or task$ or program$ or combin$ or generat$ or updat$ or initial$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:02
15	3238378	address or value	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:02
16	5487144	physical or mask or base or length	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:03

| 17 | 758 | ((("6505279") or ("5079737") or ("5628022") or ("6192455") or ("6085296") or ("6148379") or ("6499123") or ("6339816") or ("6125430") or ("5852717") or ("5386552") or ("4037214") or ("6249872") or ("6055637") or ("5757919") or ("5737760") or ("6292874") or ("6272533") or ("6339815") or ("5872994") or ("6301646") or ("6321314") or ("20010027527") or ("EP930567A") or ("WO9834365A") or ("4307214") or ("4366537") or ("5075842") or ("5890189") or ("5987557") or ("5255379") or ("5459869") or ("5615263") or ("5717903") or ("5764969") or ("2001003745") or ("4278837") or ("4521852") or ("5022077") or ("5293424") or ("5421006") or ("5473692") or ("5479509") or ("5568552") or ("5657445") or ("5729760") or ("5796845") or ("5805712") or ("5835594") or ("5844986") or ("5937063") or ("5978481") or ("6014745") or ("6058478") or ("6158546") or ("6175925") or ("4319323") or ("4759064") or ("4795893") or ("5007082") or ("5293424") or ("5317705") or ("5459867") or ("5504922") or ("5606617") or ("5901225") or ("5970147") or ("6061794") or ("6092095") or ("6115816") or ("6212635") or ("6222923") or ("6269392") or ("6330670") or ("6357004") or ("6363485") or ("6374286") or ("6378072") or ("6389537") or ("6412035") or ("6421702") or ("6445797") or ("6463537") or ("6535988") or ("6557104") or ("6633981") or ("DE4217444") or ("EP473913") or ("4571672") or ("5455909") or ("5953502") or ("6101584") or ("6633963") or ("6292569") or ("6507904") or ("6289462") or ("6157721") or ("5421006") or ("5361359") or ("5657445") or ("5493279") or ("6088451") or ("6253193") or ("6332135") or ("6336105") or ("6338050") or ("6363488") or ("6389402") or ("6427140") or ("6182227") or ("5185692") or ("5542044") or ("5276735") or ("5499297") or ("5502766") or ("5533123") or ("6138239") or ("6233576") or ("4926476") or ("6192512") or ("5892903") or ("6272533") or ("6430561") or ("6453345") or ("6405318") or ("5648648") or ("5910988") or ("6032137") or ("4624600") or ("5392357") or ("6038551") or ("6070242") or ("6266413")).PN. or ((2001/0021969) or (WO97/29567) or (WO01/75565) or (WO01/27723) or (WO01/27527)).CCLS.) or ((("6289455") or ("6477651") or ("4432057") or ("5535386") or ("5796999") or ("5893117") or ("6321263") or ("4941090") or ("5202826") or ("5287268") or ("5291003") or ("5764742") or ("5796832") or ("5870733") or ("RE36116") or ("6002605") or ("6005939") or ("6128602") or ("6137710") or ("6308277") or ("6611810") or ("6067618") or ("5280619") or ("5534855") or ("5751956") or ("5801364") or ("5812991") or ("5844218") or ("5870546") or ("6023684") or ("6052781") or ("6061057") or ("6134591") or ("6141006") or ("6189030") or ("6385724") or ("6466966") or ("6477559") or ("5428795") or ("5483649") or ("4423287") or ("6134661") or ("5301231") or ("5343529") or ("6070190") or ("6332193") or ("6468160") or ("5265163") or ("5289540") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/01/09 14:03 |

18	445999	(secur$5 or protect$ or integrit$ or guard$ or valid$) with (transaction$ or access$5 or exchang$ or input$ or output$ or translation$ or entry or enter$ or exit$ or enabl$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:05
19	2481906	(information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:09
20	407122	((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with (stor$5 or cach$5 or memory or register$ or spreadsheet$ or databas$ or buffer$ or bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:26
21	895924	(isolated or external or normal or logical or state or separate) with (execution$ or task$ or program$ or combin$ or generat$ or updat$ or initial$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:27
22	389222	(address or value) with (physical or mask or base or length)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:28
23	36278	(detector$ or sensor$ or monitor$ or check$ or snoop$ or manager$) with ((address or value) with (physical or mask or base or length))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:29
24	17426	((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with ((secur$5 or protect$ or integrit$ or guard$ or valid$) with (transaction$ or access$5 or exchang$ or input$ or output$ or translation$ or entry or enter$ or exit$ or enabl$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:31
25	4863	(((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with ((secur$5 or protect$ or integrit$ or guard$ or valid$) with (transaction$ or access$5 or exchang$ or input$ or output$ or translation$ or entry or enter$ or exit$ or enabl$5))) with (((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with (stor$5 or cach$5 or memory or register$ or spreadsheet$ or databas$ or buffer$ or bus))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/09 14:32

| 27 | 1 | (((((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with ((secur$5 or protect$ or integrit$ or guard$ or valid$) with (transaction$ or access$5 or exchang$ or input$ or output$ or translation$ or entry or enter$ or exit$ or enabl$5))) with (((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with (stor$5 or cach$5 or memory or register$ or spreadsheet$ or databas$ or buffer$ or bus))) with ((detector$ or sensor$ or monitor$ or check$ or snoop$ or manager$) with ((address or value) with (physical or mask or base or length)))) and (((("6505279") or ("5079737") or ("5628022") or ("6192455") or ("6085296") or ("6148379") or ("6499123") or ("6339816") or ("6125430") or ("5852717") or ("5386552") or ("4037214") or ("6249872") or ("6055637") or ("5757919") or ("5737760") or ("6292874") or ("6272533") or ("6339815") or ("5872994") or ("6301646") or ("6321314") or ("20010027527") or ("EP930567A") or ("WO9834365A") or ("4307214") or ("4366537") or ("5075842") or ("5890189") or ("5987557") or ("5255379") or ("5459869") or ("5615263") or ("5717903") or ("5764969") or ("2001003745") or ("4278837") or ("4521852") or ("5022077") or ("5293424") or ("5421006") or ("5473692") or ("5479509") or ("5568552") or ("5657445") or ("5729760") or ("5796845") or ("5805712") or ("5835594") or ("5844986") or ("5937063") or ("5978481") or ("6014745") or ("6058478") or ("6158546") or ("6175925") or ("4319323") or ("4759064") or ("4795893") or ("5007082") or ("5293424") or ("5317705") or ("5459867") or ("5504922") or ("5606617") or ("5901225") or ("5970147") or ("6061794") or ("6092095") or ("6115816") or ("6212635") or ("6222923") or ("6269392") or ("6330670") or ("6357004") or ("6363485") or ("6374286") or ("6378072") or ("6389537") or ("6412035") or ("6421702") or ("6445797") or ("6463537") or ("6535988") or ("6557104") or ("6633981") or ("DE4217444") or ("EP473913") or ("4571672") or ("5455909") or ("5953502") or ("6101584") or ("6633963") or ("6292569") or ("6507904") or ("6289462") or ("6157721") or ("5421006") or ("5361359") or ("5657445") or ("5493279") or ("6088451") or ("6253193") or ("6332135") or ("6336105") or ("6338050") or ("6363488") or ("6389402") or ("6427140") or ("6182227") or ("5185692") or ("5542044") or ("5276735") or ("5499297") or ("5502766") or ("5533123") or ("6138239") or ("6233576") or ("4926476") or ("6192512") or ("5892903") or ("6272533") or ("6430561") or ("6453345") or ("6405318") or ("5648648") or ("5910988") or ("6032137") or ("4624600") or ("5392357") or ("6038551") or ("6070242") or ("6266413")).PN. or (WO99/21089) or (WO97/29567) or (WO01/75565) or (WO01/27723) or | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/01/09 14:34 |

| 26 | 20 | ((((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with ((secur$5 or protect$ or integrit$ or guard$ or valid$) with (transaction$ or access$5 or exchang$ or input$ or output$ or translation$ or entry or enter$ or exit$ or enabl$5))) with (((information or parameter or detail or mode or type or number or minimum or maximum or limit) with (configuration$ or set$4 or architectur$ or structur$ or design$ or count$5)) with (stor$5 or cach$5 or memory or register$ or spreadsheet$ or databas$ or buffer$ or bus))) with ((detector$ or sensor$ or monitor$ or check$ or snoop$ or manager$) with ((address or value) with (physical or mask or base or length))) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/01/09 14:35 |